

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Andrew Graham et al. Examiner: Andres Lopez Esquerra
Serial No.: 10/533,550 Group Art Unit: 2818
Filed: November 17, 2005 Docket No.: I432.116.101/P29858
Title: VERTICALLY INTEGRATED FIELD-EFFECT TRANSISTOR ARRAY AND
METHOD FOR FABRICATING (as amended)

AMENDMENT AND RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This Amendment and Response is in reply to the Non-Final Office Action mailed April 19, 2007. Please amend the above-identified patent application as follows: